

METHOD SYSTEM AND APPARATUS FOR FLOORPLANNING PROGRAMMABLE
LOGIC DESIGNS

ABSTRACT

A method (600) of designing a programmable logic device can include the steps of identifying a cost function that penalizes floorplans of a circuit design that do not fit on the programmable logic device (605) and defining modules having components of a same type (615). A set of shapes associated with a module can be determined (610). The circuit design can be annealed (620) to determine a floorplan using the cost function and the set of shapes for the module.